Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		drive\$6 same circuit\$6 and driv\$6 same pixel\$1 and array\$6 same plural\$6 same pixel\$1 and drive\$6 same circuit\$6 and output\$6 same terminal\$1 same connect\$6 same gate same line\$1 and arrang\$6 same pixel\$1 same array\$6 and driv\$6 same switch\$6 same transistor same pixel\$1 same array\$6 the pixel having the "switchin.q" transistor being selected by the output from the output terminal; a drive transistor for transferring a gate selecting signal to the output terminal; and one or more control transistors for switching the drive transistor in response to one or more control signals, the output from the one or more control transistors being provided to the ".qate" terminal of the drive "transistor.T" the drive tran¢ictor, the control tran¢istor¢ and the tran¢ictor in the pixel array being a thin film tran¢ictor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 19:39
S1	5914	drive\$6 same circuit\$6 and driv\$6 same pixel\$1 and array\$6 same plural\$6 same pixel\$1 and drive\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 12:29
S2	235	drive\$6 same circuit\$6 and driv\$6 same pixel\$1 and array\$6 same plural\$6 same pixel\$1 and drive\$6 same circuit\$6 and output\$6 same terminal\$1 same connect\$6 same gate same line\$1 and arrang\$6 same pixel\$1 same array\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 12:48
<b>S3</b>	39	drive\$6 same circuit\$6 and driv\$6 same pixel\$1 and array\$6 same plural\$6 same pixel\$1 and drive\$6 same circuit\$6 and output\$6 same terminal\$1 same connect\$6 same gate same line\$1 and arrang\$6 same pixel\$1 same array\$6 and driv\$6 same switch\$6 same transistor same pixel\$1 same array\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 14:02

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		data adj process\$6 and detect\$6 same start\$6 same bit\$1 and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:17
		display and active adj matrix and pixel\$1 and 345/204.ccls. and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 08:52
S1	0	drive same circuit same driving same pixel same array and output same terminal same driving same transistor and drive same transistor same transferring same gate same selecting same signal and output same terminal and control same transistors same switching same drive same transistor and response and control same signals and thin same film same "transistor."	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:26
S2	1	drive same circuit same driving same pixel same array and output same terminal same driving same transistor and drive same transistor same transferring same gate same selecting same signal and output same terminal and control same transistors same switching same drive same transistor and response and control same signals and thin same film same transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:26
S3	821	drive same circuit same driving same pixel same array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:26
S4	25	drive same circuit same driving same pixel same array and output same terminal same driving same transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:26
S5	258	drive\$6 same circuit\$5 same pixel\$1 same array\$6 and output\$6 same terminal\$1 same driv\$6 same transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:28

S6	17	drive\$6 same circuit\$5 same pixel\$1 same array\$6 and output\$6 same terminal\$1 same driv\$6 same transistor\$1 and drive\$6 same transistor\$1 same transfer\$6 same gate same select\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:29
\$7	6	drive\$6 same circuit\$5 same pixel\$1 same array\$6 and output\$6 same terminal\$1 same driv\$6 same transistor\$1 and drive\$6 same transistor\$1 same transfer\$6 same gate same select\$6 same signal\$6 and output\$6 same terminal\$1 and control\$6 same transistor\$1 same switch\$6 same drive\$6 same transistor\$1 and response\$6 and control\$6 same signal\$6 and thin same film same transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:35
S8	0	drive\$6 same circuit\$5 same pixel\$1 same array\$6 and output\$6 same terminal\$1 same driv\$6 same transistor\$1 and drive\$6 same transistor\$1 same transfer\$6 same gate same select\$6 same signal\$6 and output\$6 same terminal\$1 and control\$6 same transistor\$1 same switch\$6 same drive\$6 same transistor\$1 and respond\$6 same control\$6 same signal\$6 and thin same film same transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:35
S9	657	display and active adj matrix and pixel\$1 and 345/204.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:36
S10	518	display and active adj matrix and pixel\$1 and 345/204.ccls. and tft	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 08:52
S11	563	display and active adj matrix and pixel\$1 and 345/204.ccls. and (tft or thin adj film adj transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/11 16:37
S12	11	display and active adj matrix and pixel\$1 and 345/204.ccls. and (tft or thin adj film adj transistor\$1) and (multiplex\$6 or demultiplex\$6) same driv\$6 same gate same input\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:44

S13	3	display and active adj matrix and pixel\$1 and 345/204.ccls. and tft and output\$6 same terminal\$1 same driv\$6 same transistor\$1 and drive\$6 same transfer\$6 same gate same select\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 08:53
S14	106	martin same john same edwards	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:16
S15	43	martin same john same edwards and matrix	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:05
S16	13	martin same john same edwards and matrix same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:05
S17	0	martin same john same edwards and matrix same display same deivce	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:05
S18	11	martin same john same edwards and matrix same display same device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:16
S19	11	martin same john same edwards and matrix same display same device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:16
S20	67	karel same elbert same kuijk	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:17
S21	34	karel same elbert same kuijk and matrix same display same device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 09:17

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S22	39	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) and (multiplex\$6 or demultiplex\$6) same driv\$6 and pull\$6 same up same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:49
S23	32	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) and (multiplex\$6 or demultiplex\$6) same driv\$6 and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:51
S24	5	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) and (multiplex\$6 or demultiplex\$6) same driv\$6 same pixel\$1 and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:54
S25	5	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and (multiplex\$6 or demultiplex\$6) same driv\$6 and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:57
S26		display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and (demultiplex\$6) same driv\$6 and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:58
S27	0	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and demultiplex\$6 and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:58
S28	0	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and (demultiplex\$6 or Nor) and pull\$6 same up same gate same line\$1 and pull\$6 same down same gate same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:58

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S29	1304	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and (demultiplex\$6 or Nor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:58
S30	103	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and demultiplex\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 11:59
S31	2	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) same driv\$6 same pixel\$1 and demultiplex\$6 and pull\$6 same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 12:09
S32	3	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) and driv\$6 same pixel\$1 same demultiplex\$6 and pull\$6 same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 15:39
S33	4	display and active adj matrix and driv\$6 same pixel\$1 same demultiplex\$6 and pull\$6 same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 12:13
S34	24	display and active adj matrix and pixel\$1 and (tft or thin adj film adj transistor\$1) and driv\$6 same pixel\$1 same multiplex\$6 and pull\$6 same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 15:39
S35	0	"20020101433" and multiplex6 and pull-up and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 16:06
S36	0	"20020101433" and multiplex6 and pull same up and pull same down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 16:07
S37	2	"20020101433"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 16:07
S38	1	"20020101433" and driv\$6 same pixel\$1 same multiplex\$6 and pull\$6 same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/13 16:08

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S39	2	"20020075211"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON.	2006/11/14 07:55
S40	2	"20010054997"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:03
S41	2	"20030048249"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:49
S42	7048	data adj process\$6 and detect\$6 same start\$6 same bit\$1 and start\$6 same synchroniz\$6 adj signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 adj clock\$6 and start\$6 adj count\$6 and count\$6 same complete\$6 and stop\$6 same count\$6 and start\$6 same output\$6 smae last same latch\$6 same data and detect\$6 same stop\$6 same bit same last same latch\$6 and stop\$6 same propagt\$6 same signal\$6 and stop\$6 same synchroniz\$6 adj signal\$6 and stop\$6 same output\$6 smae last same latch\$6 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:16
S43	396827	data adj process\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:17
S44	6301	data adj process\$6 and detect\$6 same start\$6 same bit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:18
S45	1519	data adj process\$6 and detect\$6 same start\$6 same bit\$1 and start\$6 same synchroniz\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:18
S46	131	data adj process\$6 and detect\$6 same start\$6 same bit\$1 and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:19

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S47	1	data adj process\$6 and detect\$6 same start\$6 same bit\$1 and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 adj clock\$6 and start\$6 adj count\$6 same complete\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:20
S48	711	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:21
S49	266	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:21
S50	29	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:21
S51	0	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 adj clock\$6 and start\$6 adj count\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:22
S52	27	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:23
S53	26	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same count\$6 and count\$6 same complete\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:23
S54	16	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:24

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S55	16	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:25
S56	0	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6 and start\$6 same output\$6 same last same latch\$6 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:26
S57	16	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6 and start\$6 same output\$6 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:27
S58	0	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same data and start\$6 same output\$6 same data and detect\$6 same stop\$6 same bit and stop\$6 same propagat\$6 same signal\$6 and stop\$6 same synchroniz\$6 same signal\$6 and stop\$6 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:29
S59	15	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6 and start\$6 same output\$6 same data and detect\$6 same stop\$6 same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:29

S60	1	data adj process\$6 and detect\$6 same (start\$6 adj bit\$1) and start\$6 same synchroniz\$6 same signal\$6 and	US-PGPUB; USPAT; EPO; JPO;	OR	ON	2006/11/14 09:30
		start\$6 same propagat\$6 same signal\$6 and start\$6 same clock\$6 and start\$6 same count\$6 and detect\$6 same count\$6 same complete\$6 and start\$6 same output\$6 same data and detect\$6 same stop\$6 same bit and stop\$6 same propagat\$6 same signal\$6	DERWENT; IBM_TDB			